

- oscillator circuit, 356
 - oscillator tolerance, 356–357
 - period limitations, 162
 - PLL, *see* PLL
 - power filtering, 357, 364
 - quartz crystal, 356
 - RC oscillator, 273–274
 - reference for UART, 101
 - signal integrity, 358–359, 409
 - skew, 25–27, 358, 367
 - skew in source-synchronous interface, 181
 - speed of microprocessor, 58
 - standard RS-232 frequencies, 104
 - synchronization in UART, 101
 - tree, 357–358, 367
 - zero delay distribution, 362–363
 - clock-to-out time, flip-flop, *see* flip-flop
 - CMOS (complementary MOS)
 - EPROM, 80
 - semiconductor process, 38, 308
 - SRAM, 87–88
 - coil, *see* inductor
 - collector, BJT, *see* transistor
 - collision detection, 113, 217
 - combinatorial logic, 22, 224–225
 - comma pattern, *see* channel coding
 - common mode signal, 290
 - comparator, *see* op-amp
 - compliment, logical, 4
 - computer, *see also* microprocessor
 - defined, 56
 - design example, 63–67
 - DMA, 68–70
 - performance metric, 169–172
 - physical and virtual memory, 158
 - use of DRAM, 90
 - Condor, 390
 - Conexant, 199
 - Conner-Winfield, 357
 - conservation of charge, 267
 - context switch, 139
 - Corelis, 433
 - Coulomb, 267, 272
 - counter
 - FSM, 237–238
 - ripple, 19–21
 - synchronous, 22–23
 - CPLD, *see* PLD
 - CPU, *see* microprocessor
 - CRC (cyclic redundancy check), 194, 207, 209–215
 - crosstalk, *see* signal integrity
 - Crystal Semiconductor, 346
 - CS (chip select), 64–65
 - CTS, 356
 - current
 - ampere, 267
 - regulator, 381, 385
 - cutoff frequency, *see* filter
 - Cypress Semiconductor, 252, 255, 257, 358
- ## D
- DAC (digital-to-analog converter)
 - current output, 349
 - operation, 341
 - R-2R ladder, 348–349
 - sampling artifacts, 344–345
 - Darlington pair, 303–304
 - data bus, 57
 - data link layer, 194
 - data memory, 56
 - data rate matching, 95–96
 - data sheet
 - FET, 307
 - logic IC, 51–54
 - op-amp, 316–317
 - Datel, 388
 - DB25, 102, 106
 - DC circuit, 274
 - DC electrical characteristics, 53
 - DCE (data communications equipment), 104
 - DDR (double data rate)
 - FPGA I/O, 261
 - SDRAM, *see* SDRAM
 - SRAM, *see* SSRAM
 - DE9, 102, 106
 - debugging, 430, 433
 - decibel, 281–282
 - decoupling capacitor, *see* capacitor
 - demodulation, 108
 - DeMorgan's Law, 8, 232
 - demultiplexer (or demux), 28
 - die, silicon, 36, 250–251
 - dielectric
 - constant, 400–401
 - flash memory bit structure, 81
 - impedance effects, 402–403
 - PCB, 408–409
 - differential

- data transmission, 107–108, 113
 - mode signal, 290
 - Digital Equipment Corporation, 216
 - diode, 34
 - clamp in reset circuit, 428–429
 - clipping circuit, 295
 - defined, 293
 - ESD protection, 417
 - forward voltage, 294
 - I-V characteristic, 294
 - LED (light emitting diode), 34, 198, 299
 - LED driving with BJT, 301–303
 - logic functions, 298–299
 - photodiode amplification, 326–327
 - power supply backup, 297–298
 - rectification, 296–297
 - Schottky, 295, 299
 - shunt voltage regulator, 377–379
 - switching regulator, 387
 - voltage reference, 294–295, 372–373
 - voltage regulator protection, 383
 - Zener voltage, 294, 377, 379
 - DIP (dual in-line package), 38, 80
 - direct addressing, 74
 - DIX Ethernet, *see* Ethernet
 - DLL (delay locked loop), 366–367
 - FPGA, 259–260
 - DMA (direct memory access)
 - bus control, 142
 - controller (DMAC), 68–70
 - system performance, 171
 - DMM (digital multimeter), 441
 - DMOS (double-diffused MOS), 309
 - don't care, logical usage, 10
 - doping, 34, 293
 - drain, FET, *see* transistor
 - DRAM (dynamic RAM)
 - bit structure, 88
 - burst transactions to cache, 156
 - CAS*, 88–89
 - defined, 78
 - EDO, 91
 - FPM, 90
 - internal structure, 88–89
 - performance versus SRAM, 149
 - RAS*, 88–89
 - rate matching with FIFO, 95–96
 - refresh, 88–89
 - synchronous, *see* SDRAM
 - DSP (digital signal processor), 167–169
 - D-subminiature connector family, 106
 - DTE (data terminal equipment), 104
 - dual-port memory, 93
 - duty-cycle, 18
- E**
- Ecliptek, 356
 - ECS, 356
 - EEPROM (electrically erasable programmable ROM), 85–86
 - PLD programming, 253
 - EIA/TIA (Electronics Industry Association and Telecommunications Industry Association), 102
 - electric potential, 267
 - EMI (electromagnetic interference)
 - radiation, 413
 - signal integrity, 410–412
 - emitter, BJT, *see* transistor
 - energy, 267
 - EPROM (erasable programmable ROM)
 - asynchronous interface, 80–81
 - defined, 79
 - OTP (one-time programmable), 80
 - programming voltage, 79
 - silicon bit structure, 79
 - equality comparison, 12
 - error detection, 99, 115–116, 194, 207
 - checksum, 207
 - CRC, 207
 - ESD (electrostatic discharge), 415–417
 - Ethernet, 199, 215–219
 - 8B10B coding, *see* channel coding
 - autonegotiation, 218
 - collision detection, 217
 - CRC-32, *see* CRC
 - DIX, 216
 - frame format, 216–217
 - jumbo frame, 217
 - MAC (media access controller), 194, 216–217
 - MAC address, 194, 216
 - preamble, 216
 - twisted-pair wiring, 197
 - Exemplar, 244
 - exponent, floating-point, 165
- F**
- Fairchild Semiconductor, 307, 316–317, 384
 - falling-edge triggered, flip-flop, *see* flip-flop